

to the ground power source line, so no noise is generated on the first power source.

Please REPLACE the paragraph beginning at page 11, line ¹⁷~~27~~, as follows:

The control circuit 30 has a display data control portion 32, a scanning driver control portion 34, a common driver control portion 36, and so on, and is supplied with clock pulses CLK, display data DATA, vertical synchronization signals Vsync, horizontal synchronization signals Hsync, and so on from a computer, a tuner, or the like. The display data control portion 32 receives the display data DATA and performs the required A/D conversion, intensity level adjustment, data conversion, and so forth, and supplies data signals for display to the address driver 23. The scanning driver control portion 34 supplies scanning control signals to the scanning driver 26 in synchronization with the synchronization signals. The common driver control portion 36 produces control signals for the application of write pulses or erase pulses during the reset period and for the application of sustaining pulses during the sustaining discharge period, and supplies these control signals to the drivers 25 and 28.

Please REPLACE the paragraph beginning at page 12, line 8, as follows:

Figures 4A and 4B illustrate the first drive method in this embodiment. This is an example of sustaining pulses applied between the X electrodes and Y electrodes. Figure 4A illustrates the drive waveforms of the address electrodes A and the X and Y electrodes, and Figure 4B illustrates the path of the discharge current and the drive circuit of the X and Y electrodes. With the first drive method in Figures 4A and 4B, the X and Y electrodes are both maintained at a negative first power source potential -V1 that is different from the ground power source GND, and are alternately driven to a positive second power source potential +V2 and then returned to the first power source potential -V1. To this end, power sources V2 and V1, which use the ground power source GND as a reference, are provided in the drive circuit, and the first power source potential V1 and the second power source +V2 constitute a power source line that is electrically separate from the ground power source line GND.

IN THE CLAIMS:

Please REPLACE claims, in accordance with the following:

1. ~~(AS ONCE AMENDED HEREIN) A plasma display panel device having first and~~